Unit 1: The Model of the Computer

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Today's Takeaway

• Earlier this week: computers are doing logic with gates!

• Today: Memory, Logic, and input/output = Computer!



Outline for Today

- Review of Gates
 - AND, OR, NOT, Composite Gates
 - Domino Gates, XOR, Adding with Gates
- State Machines!
- Model of a Computer









Q: What is this, physically?





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Gates: AND

	Ρ	Q	AND(Р	Q)
_	1	1	1	1	1
-	1	0	0	1	0
-	0	1	0	0	1
-	0	0	0	0	0

.....





Gates: OR













Gates: Composition





Domino Gates





Domino Gates





Could It Work?

- Michael's domino OR gate: 24 dominoes
- The first pentium processor had 3.3 Mill transistors, or roughly 800k gates.
- So we need around 20 Mill dominoes
- World record for domino topple: 4.5 Mill
- Pentium: computes 60 Mill times a second



Dominoes? Takes awhile to set up...

Two Different Notions of Speed

- 1. Electricity vs. Dominos falling over
- 2. Do we have the shortest computation for that problem?



P = NOT(NOT(P))



P = NOT(NOT(P))

2

of Gates:

()



P NOT(NOT(NOT(NOT(P))))

4

of Gates:



()

P NOT(NOT(NOT(NOT(NOT(P)))))

of Gates:

6



 $\left(\right)$

P NOT(NOT(NOT(NOT(P)))).....

of Gates:

()

6024



P NOT(NOT(NOT(NOT(P)))).....

of Gates: 0 6024 Think about that many domino gates...



P NOT(NOT(NOT(NOT(P)))).....



Identical logical formulas, dramatically different speed of computation!



One Last Gate... XOR

	Ρ	Q	XOR(Р	Q)
_	1	1	0	1	1
-	1	0	1	1	0
-	0	1	1	0	1
-	0	0	0	0	0





One Last Gate... XOR

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-	1	0	1	1	0
-	0	1	1	0	1
-	0	0	0	0	0





One Last Gate... XOR

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-	0	1	1	0	1
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.....





Arithmetic and Logic

We know how to do addition, subtraction, and multiplication in binary...

But we've claimed that computers used AND, OR, NOT gates at their core to do everything...



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Q: Can we represent addition with logical gates?



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Hint: you'll need these:







Arithmetic and Gates **A**: $\left(\right)$ one's digit XOR \cap two's digit AND () $\vdash 1$ $\mathbf{0}$ 0 1







A:





A:





A:




























Binary Adder





Truth Table to Formula

Idea: with a certain set of logical functions, we can represent all possible logical formulas!



Truth Table to Gate

Idea: with a certain set of logical functions gates, we can represent all possible logical formulas!





Abstraction:



Low level programs



Abstraction:





Abstraction:





State Machines









Suppose we want a blinking christmas light:



O = was the light on a second ago? N = is the light on now?



Suppose we want a blinking christmas light:



oldLight = was the light on a second ago? *newLight =* is the light on now?



Suppose we want a blinking christmas light:



oldLight = was the light on a second ago?

newLight = is the light on now?

Q: How can we write the value of newLight in terms of oldLight?



Suppose we want a blinking christmas light:



oldLight = was the light on a second ago?
newLight = is the light on now?

A: newLight = NOT(oldLight)



Suppose we want a blinking christmas light:





A: newLight = NOT(oldLight)

























































OR(left,right)





OR(left,right)


A More Complicated Example: Sliding Doors



A More Complicated Example: Sliding Doors



OR(left,right)

Q: How else can we write this with logic?



A More Complicated Example: Sliding Doors



OR(left,right)

Q: How else can we write this with logic?



A: AND(NOT(left),NOT(right))

Side Note: DeMorgan's Law

AND(NOT(left),NOT(right))

=

NOT(OR(left,right))



Side Note: DeMorgan's Law

<i>P</i>	Q	AND(NOT(P)	NOT(Q))
1	1	0	0	0
1	0	0	0	1
0	1	0	1	0
0	0	1	1	1
		_		
Р	Q		NOT(OR(P,Q))
1	1		0	1
1	0		0	1
0	1		0	1
0	0		1	0

[[



Side Note: DeMorgan's Law

P	Q	AND(NOT(P) NOT(Q))
1	1	0	0	0
1	0	0	0	1
0	1	0	1	0
0	0	1	1	1
<i>P</i>	Q		NOT(OR(P,Q))
<i>P</i> 1	Q 1		NOT(0	<i>OR(P,Q))</i> 1
P 1 1	Q 1 0		NOT(0 0	<i>OR(P,Q))</i> 1 1
P 1 1 0	Q 1 0 1		NOT(0 0	<i>OR(P,Q))</i> 1 1
P 1 1 0 0	Q 1 0 1 0		NOT(0 0 0 1	<i>OR(P,Q))</i> 1 1 0



State Machines: Sliding Doors





State Machines: Sliding Doors



Sensors! Input! Reading from Memory!



- Internal states (memory)
- Complex logic relating bits/information
- Mechanism for setting bits (input)
- Mechanism for displaying bits (output)























https://xkcd.com/505/



Alonzo Church



Alan Turing





"There is only one notion of computation"

(and anything with these properties is doing it)

- Internal states
- Complex logic relating bits
- Mechanism for setting bits (input)



Mechanism for displaying bits (output)

























- Some other questions:
 - Is the human brain carrying out computation?
 - Is the comic right? Or are there things in the world that feel like they aren't computation?
 - Is "randomness" computable? How?
 - Is there one "fastest" model of a computer? (e.g. X is to the transistor as the transistor is to the domino)



- Internal states (memory)
- Complex logic relating bits
- Mechanism for setting bits (input)
- Mechanism for displaying bits (output)





- Internal states
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Internal states

- Complex logic relating bits
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Internal states

Complex logic relating bits

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Internal states

Complex logic relating bits

Mechanism for setting bits (input)

Mechanism for displaying bits (output)





Monitor

- Internal states
 - Complex logic relating bits
 - Mechanism for setting bits (input)

Mechanism for displaying bits (output)



Next Time



Monitor

Internal states

Complex logic relating bits

Mechanism for setting bits (input)

Mechanism for displaying bits (output)

Programming lets us define the "complex logic"!

